

SCOPE

PROGRAM YOUR SUMMER TO PROGRAM

SUMMER CAMP ON PROGRAMMING FOR ELECTRONICS ENGINEERS



For Details,

Contact: Prof. R. Prakash
Division Leader, Embedded System Division
School of Electronics Engineering, VIT University
Contact No: 9486265296, EMAIL : rprakash@vit.ac.in

Course description:

Phase-I [Linux]

Day1 24/06/2010 [Thursday]

- Introduction to OS
- OS Services
- Where does the operating System fits in – In embedded System
- Introduction to Linux/Unix
- Linux commands – A complete overview

Day2 25/06/2010 [Friday]

- Linux Shell Scripting-A complete demo and practical sessions
- Linux C Programming- An Eye opener
- Unix system programming-Practical and Theoretical approach

Resource person: Prof. K. V. Shriram (LINUX OS and material softcopy will be provided)

Phase-II [ARM Processor]

Day1 26/06/2010 [Saturday]

- Arm Architecture and Organization
- ARM/THUMB Programming model
- ARM/Thumb Instruction set
- ARM/Thumb Assembly in Keil [Practical]

Day2 27/06/2010 [Sunday]

- ARM Thumb interworking [practical]
- ARM Exception Handling
- ARM C programming [GPIO] in Keil [Practical]
- ARM GNU Tool – An intro [Practical]

Resource person: Prof. B.Karthikeyan

(Evaluation Tools and material softcopy will be provided)

Phase-III [VeriLog]

Day1 28/06/2010 [Monday]

- VLSI Design Flow [XILINX based]
- VeriLog Intro
- combinational circuits design using VeriLog

Day2 29/06/2010 [Tuesday]

- Modeling of Sequential Logic using VeriLog (Flip flop, Shift Registers, Counters)
- Modeling of state machine using VeriLog (Sequence Detector, Traffic light Controller, Elevator operation)

Resource person: Prof.C.Vivek (material softcopy will be provided)

Course Duration:

6 Days – June 24th to 29th 2010.

Phase-I [Linux] : June 24th and 25th
Phase-II [ARM Processor] : June 26th and 27th
Phase-III [VeriLog HDL] : June 28th and 29th

Last date for Registration:

On or before : 18-06-2010

Course Fee:

Phase: I II III
Fees : Rs 600/- Rs 600/- Rs 600/-

Accommodation: 200/day

Note: DD should be taken in favor of **VIT University** payable at **Vellore**.

SEND THE REGISTRATION FORM AND DD TO:

PROF. R. PRAKASH
DIVISION LEADER
EMBEDDED SYSTEM DIVISION
SCHOOL OF ELECTRONICS ENGINEERING.
VIT UNIVERSITY
VELLORE – 632014. TN
MAIL : rprakash@vit.ac.in
Contact : 9486265296

Registration Form

Name :

Department :

Organization :

Experience :

Address for Correspondence:

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Email :

Phone :

Accommodation Required Yes No

Faculty Student

Phases want to attend: Phase-I Phase-II Phase-III

Demand Draft Details:

Amount :

DD No. :

Bank :

Dated :

Signature :

(Use Photocopy of the above form, if required)