

REGISTRATION FORM

A Short Term Course on Embedded DSP System

Organised by
Signal & Image Processing Division
& Embedded System Division
School of Electronics Engineering
VIT University, Vellore, T.N.India

2nd & 3rd April 2010

Name :

Designation :

Organization :

Address :

Contact No :

E-mail :

Accommodation is Required : Yes / No

Payment Details

Amount :

DD Number :

Name of the Bank & Place :

Date :

Signature of the Participant

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School of Electronics Engineering

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School of Electronics Engineering



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UNIVERSITY
(Estd. u/s 3 of UGC Act 1956)

Vellore - 632 014, Tamil Nadu, India

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School of Electronics Engineering
VIT University
Vellore -632014
Tamil Nadu, India.
www.vit.ac.in



VIT - A place to learn; A chance to grow

ABOUT THE VIT UNIVERSITY

VIT University is the number one Private Engineering institute of the country as per the AC Neilson Org Marg survey done by the India Today. VIT is the first educational institution in India to get international accreditations for its programmes. The Institution of Engineering and Technology (IET), UK and the Energy Institute, UK have audited the Teaching-Learning Processes at VIT and accredited the engineering programmes with the highest validity of 5 years. VIT is the first university in India to get accredited by the Accreditation Board for Engineering and Technology (ABET) of USA for its Civil and Mechanical undergraduate programmes with maximum years of validity.

ABOUT SCHOOL OF ELECTRONICS ENGINEERING

The School offers a Bachelor's level programme in Electronics and Communication and Master's programmes in Communication Engineering, Sensor System Technology, Automotive Electronics, VLSI Design and Nanotechnology. The School also offers M.S. and Ph.D programs in nearly 20 areas of specialization. All engineering programmes of the school at B.tech and M.Tech levels have been accredited by the Institute of Engineering and Technology, U.K

ABOUT SHORT TERM COURSE

Development of Embedded Systems has lot of challenges and the design and development of such systems for DSP application like speech processing and image processing requires complete understanding of DSP as applicable to industry. A technical talk on modalities of DSP by academicians and industry experts is vital for faculty, student and research community and so this short term course.

TOPICS TO BE COVERED

- ✓ Application of DSP in Communication System
- ✓ Advances in Computer Vision & Image Processing
- ✓ VLSI DSP
- ✓ Trends and Issues in Embedded DSP

RESOURCE PERSONS

Prof.(Dr).S.J Thiruvankadam
Thaigarajar College of Engineering, Madurai.

Dr.Lokesh R Boregowda
Honeywell Technology Solution Lab,
Bangalore

Prof.(Dr).Venkataramani
National Institute of Technology
Trichy.

Experts from Texas Instruments India Ltd
and Cranes Software Bangalore.

FEE DETAILS

For Research Scholars /Student : Rs.1500
For Faculty : Rs.3000
For Industry : Rs.4000

The Registration fee is to be paid through Demand Draft drawn in favour of VIT University, payable at Vellore. The Registration fee includes Course Material, lunch and refreshment. Accommodation will be arranged only on request.

Free DSP Book authored by CHI TSONG CHEN for all participants sponsor by Oxford University Press

Accommodation

Rs.250/day including breakfast and dinner in VIT Hostels. Availability based on first Come first serve basis.

IMPORTANT DATE

Course Date: 2nd & 3rd April 2010
Last Date of Registration: 25th March 2010

ADDRESS FOR CORRESPONDENCE

For any queries and to send the completed Registration form to:

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